

Figure 1

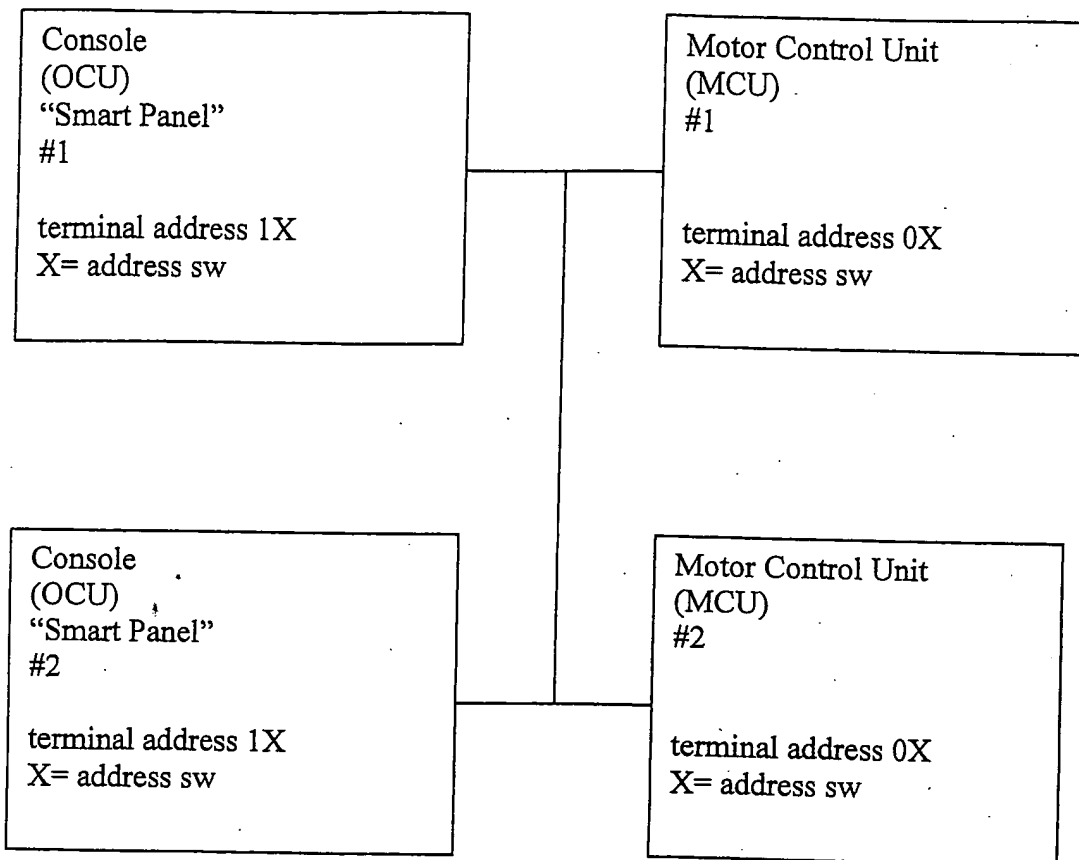


Figure 2

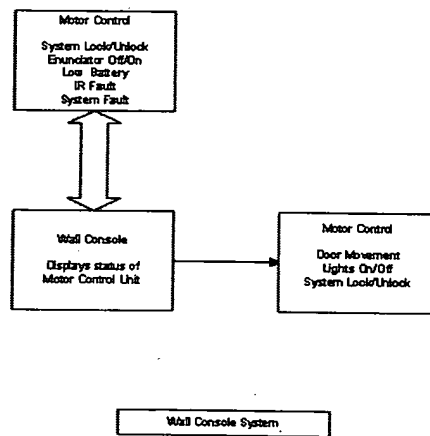


Figure 3

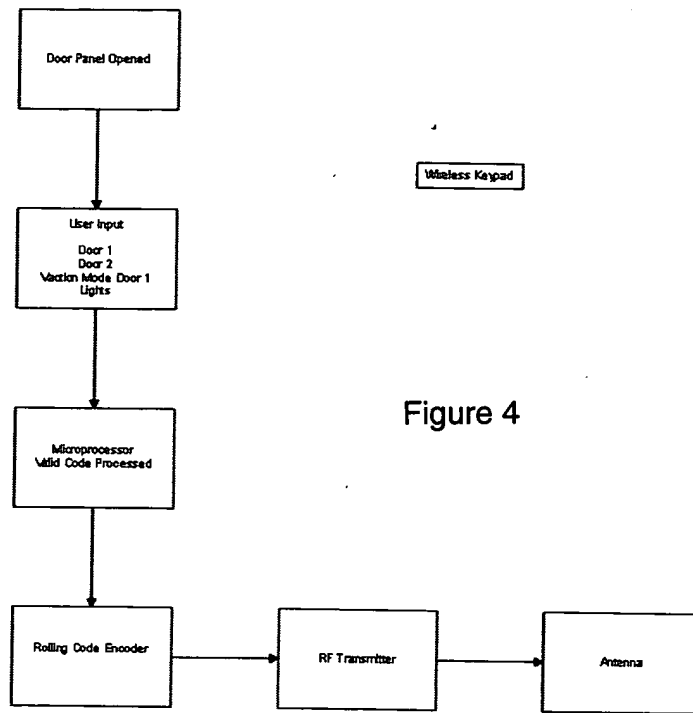


Figure 4

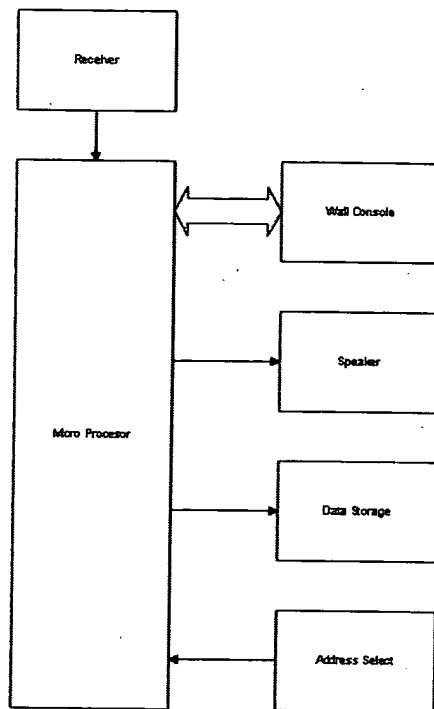
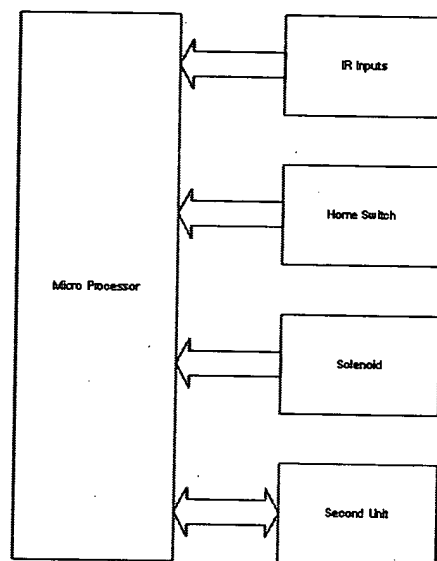


Figure 5



Motor Control Unit Page 2

Figure 6

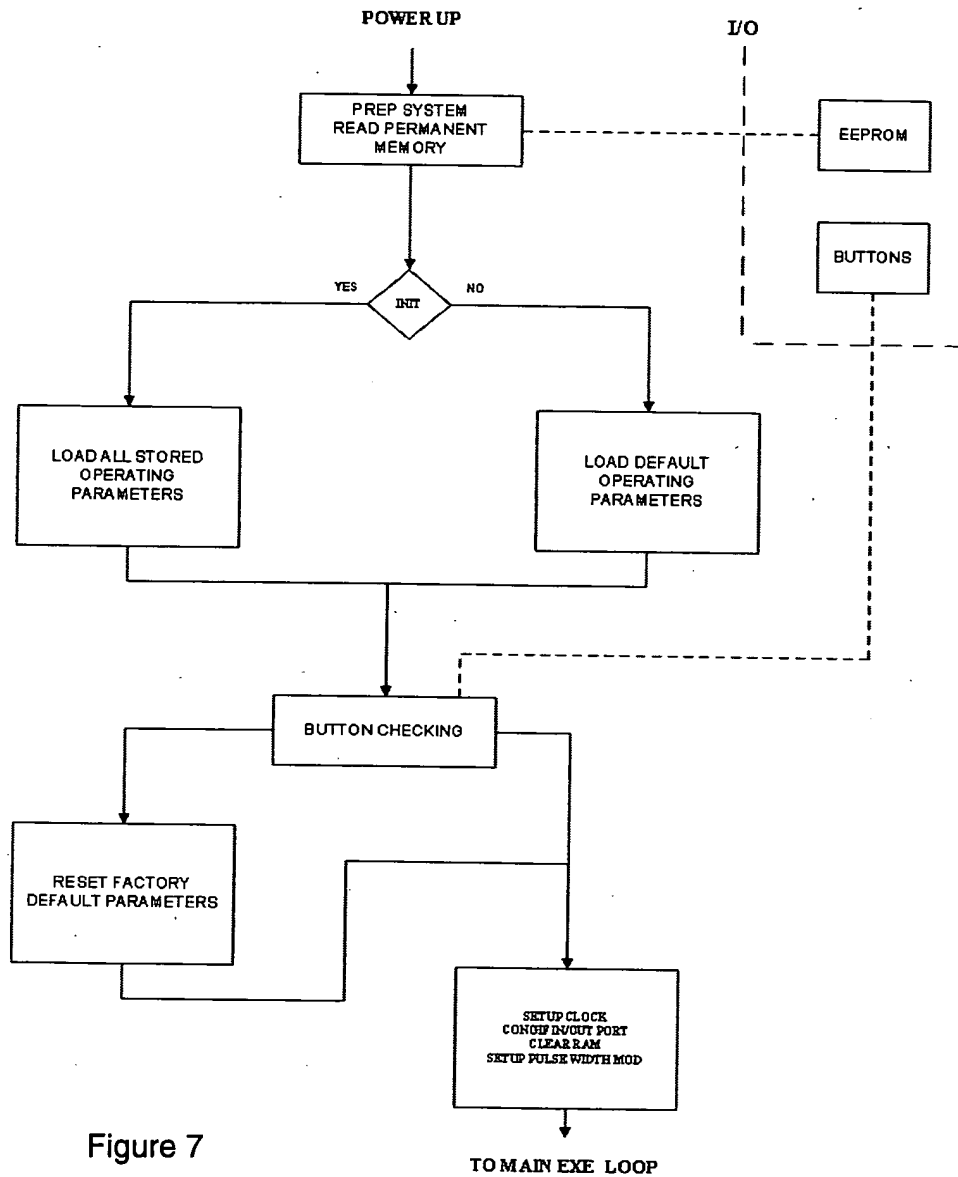


Figure 7

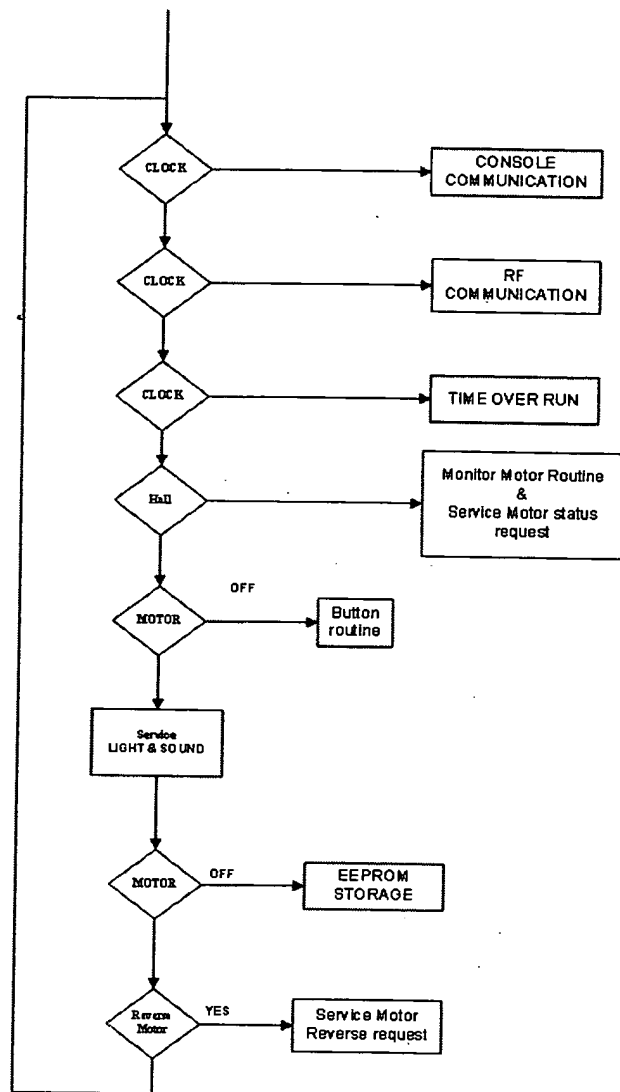


Figure 8

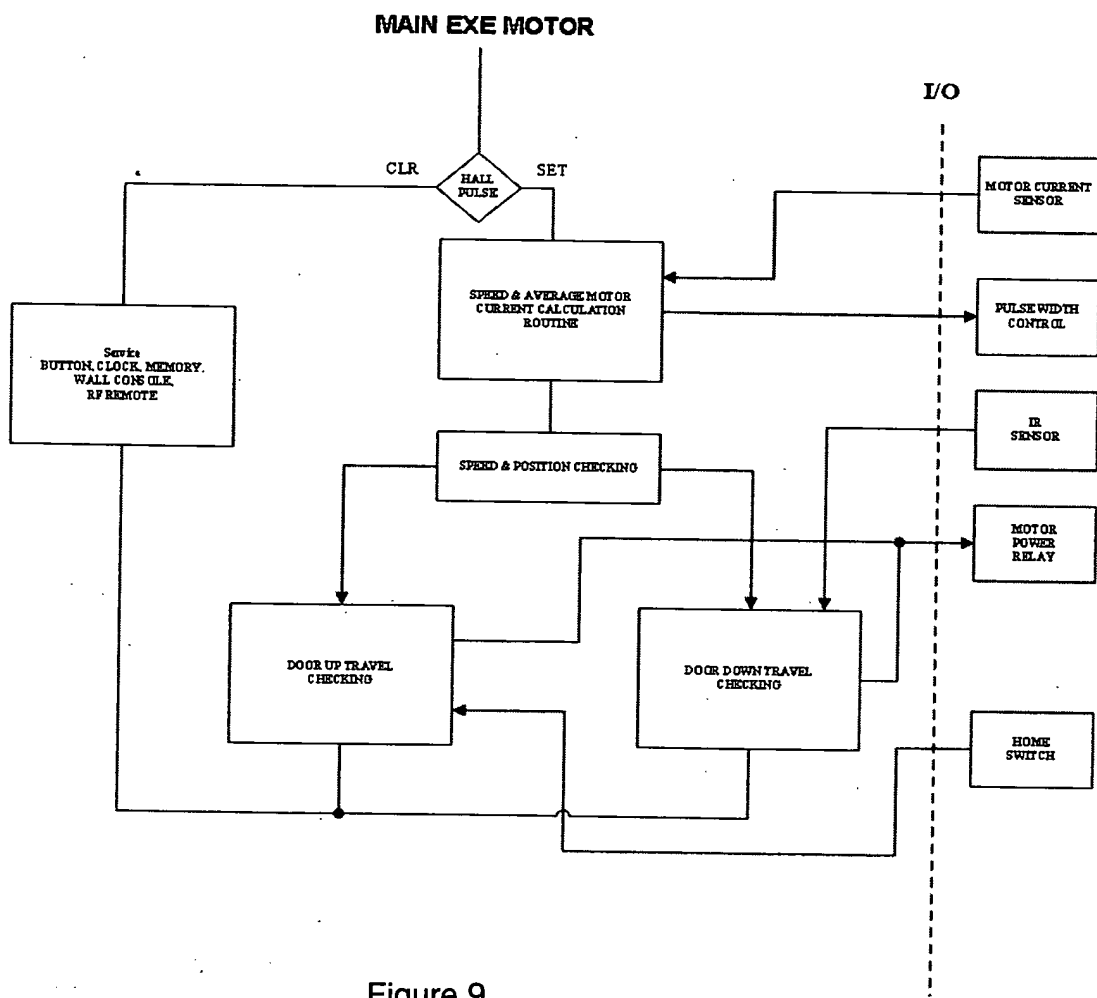


Figure 9

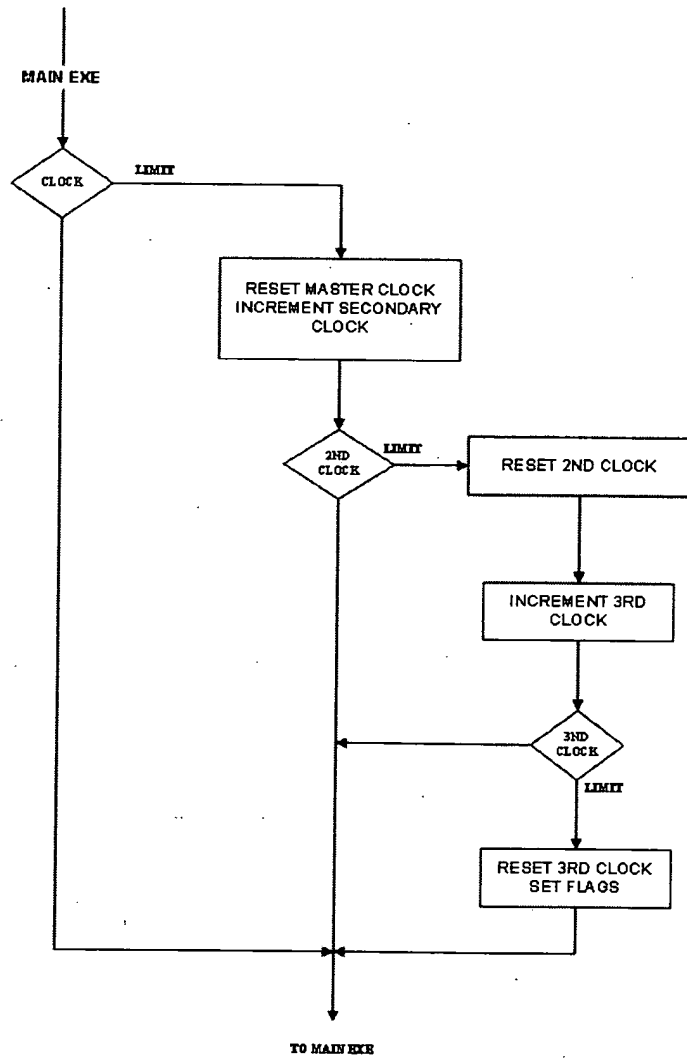


Figure 10

CALC_OLD_AVE

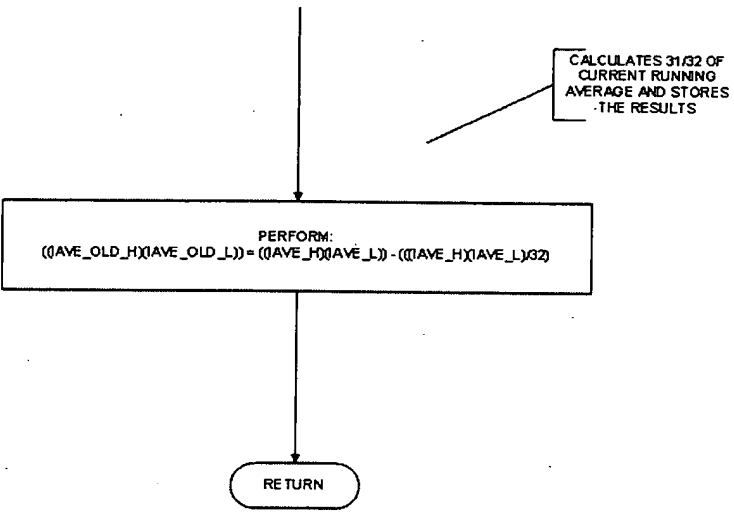


Figure 11

CALC_NEW_AVE

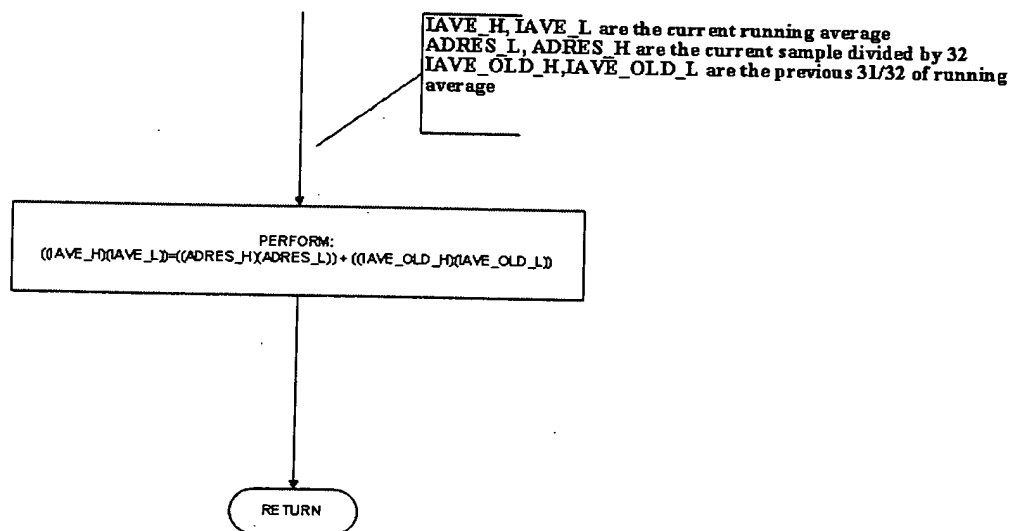


Figure 12

AD_DIV_32

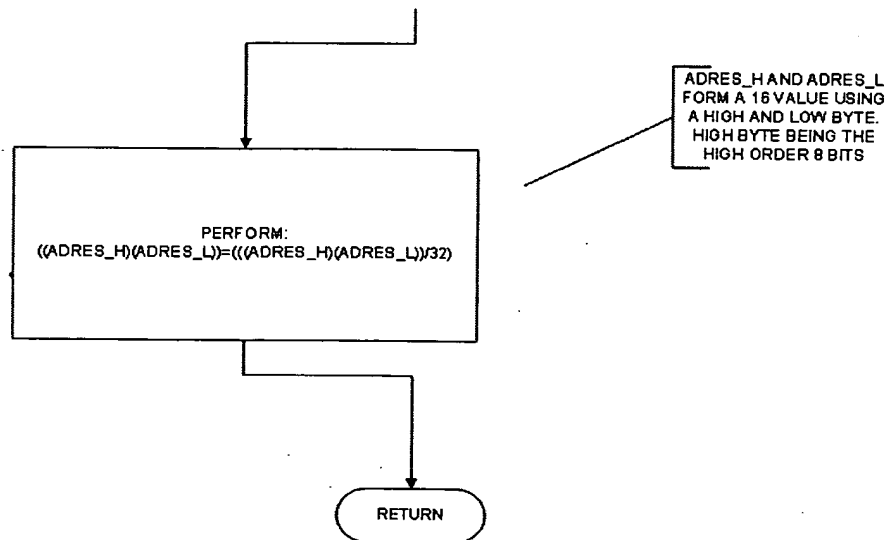


Figure 13

PWMRAMPDOWN

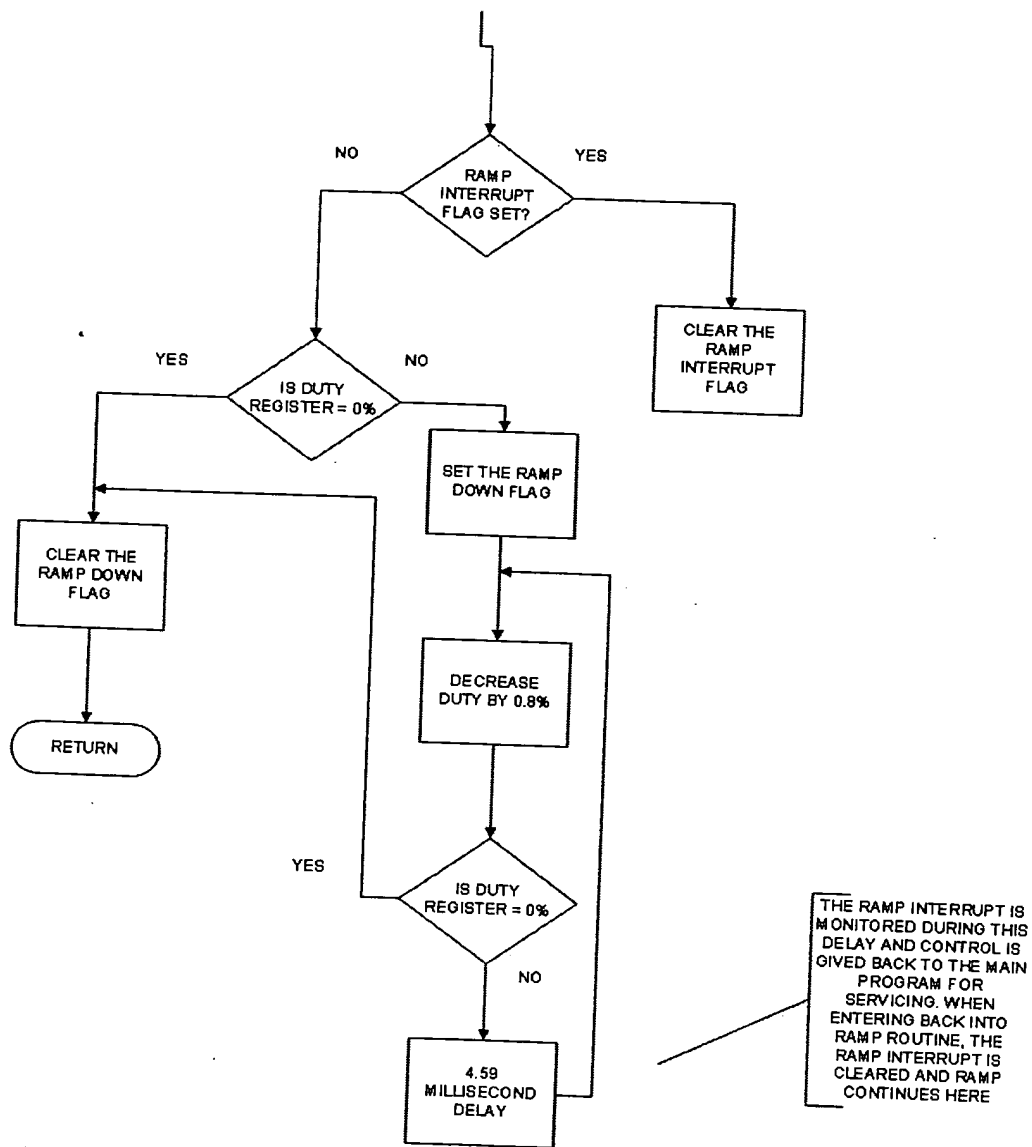


Figure 14

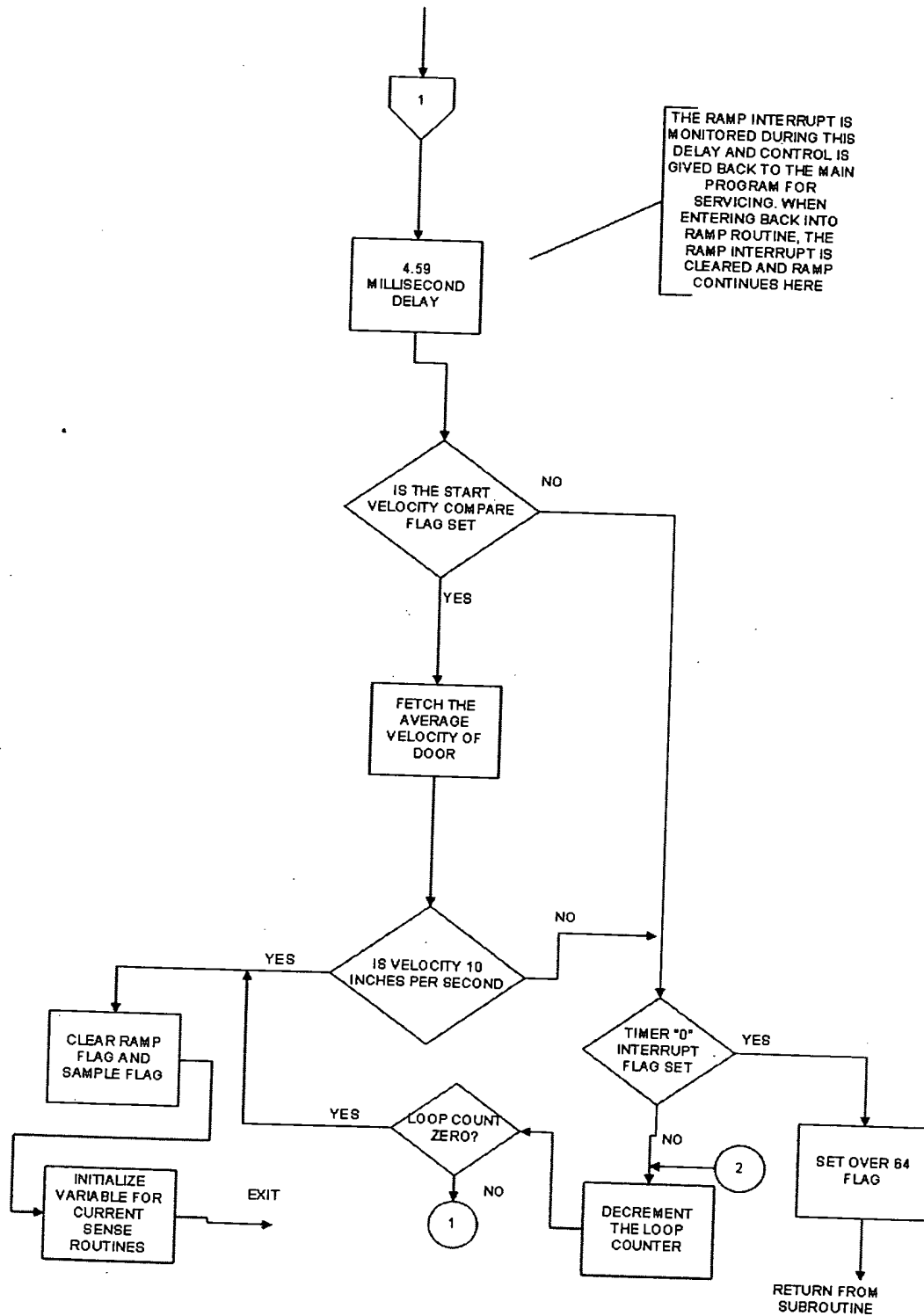


Figure 15

PWM_RAMP_INIT

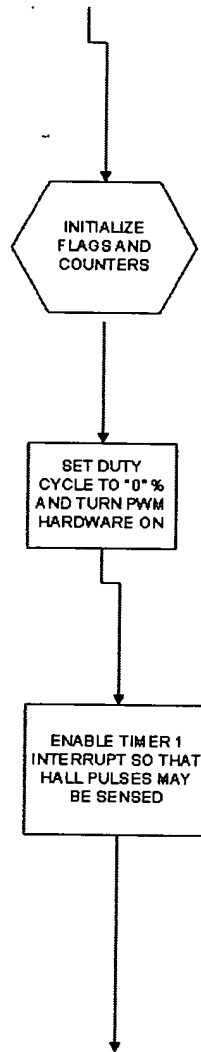


Figure 16

PWM_HW_INIT

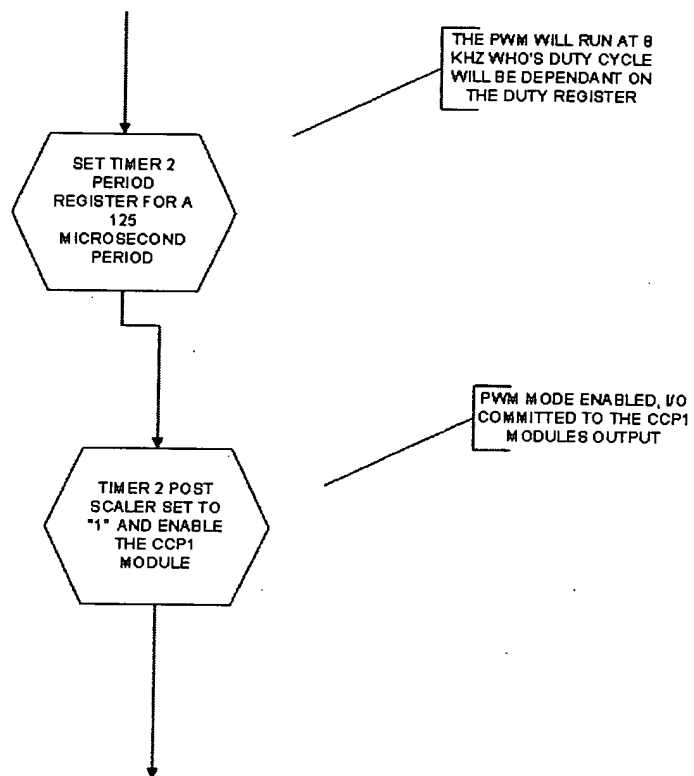
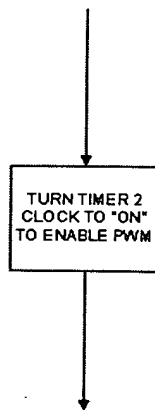


Figure 17

PWM_HW_ON



PWM_HW_OFF

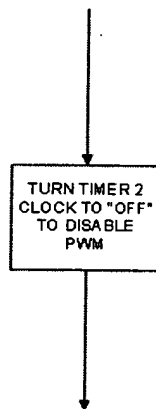


Figure 18

LOW_CLIP

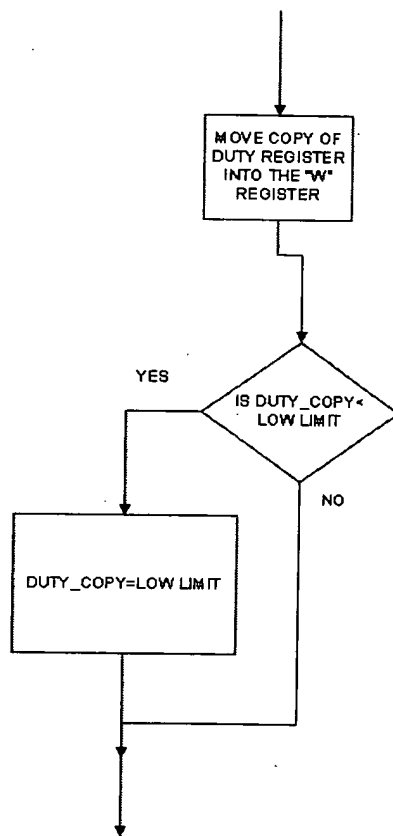


Figure 19

HIGH_CLIP

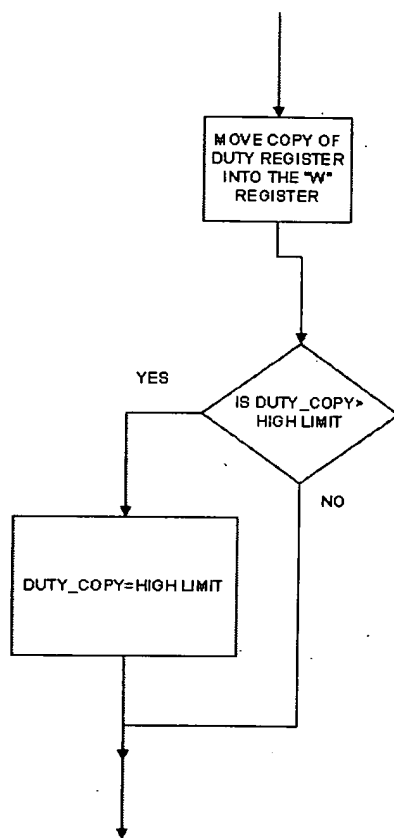


Figure 20

PWM_GAIN

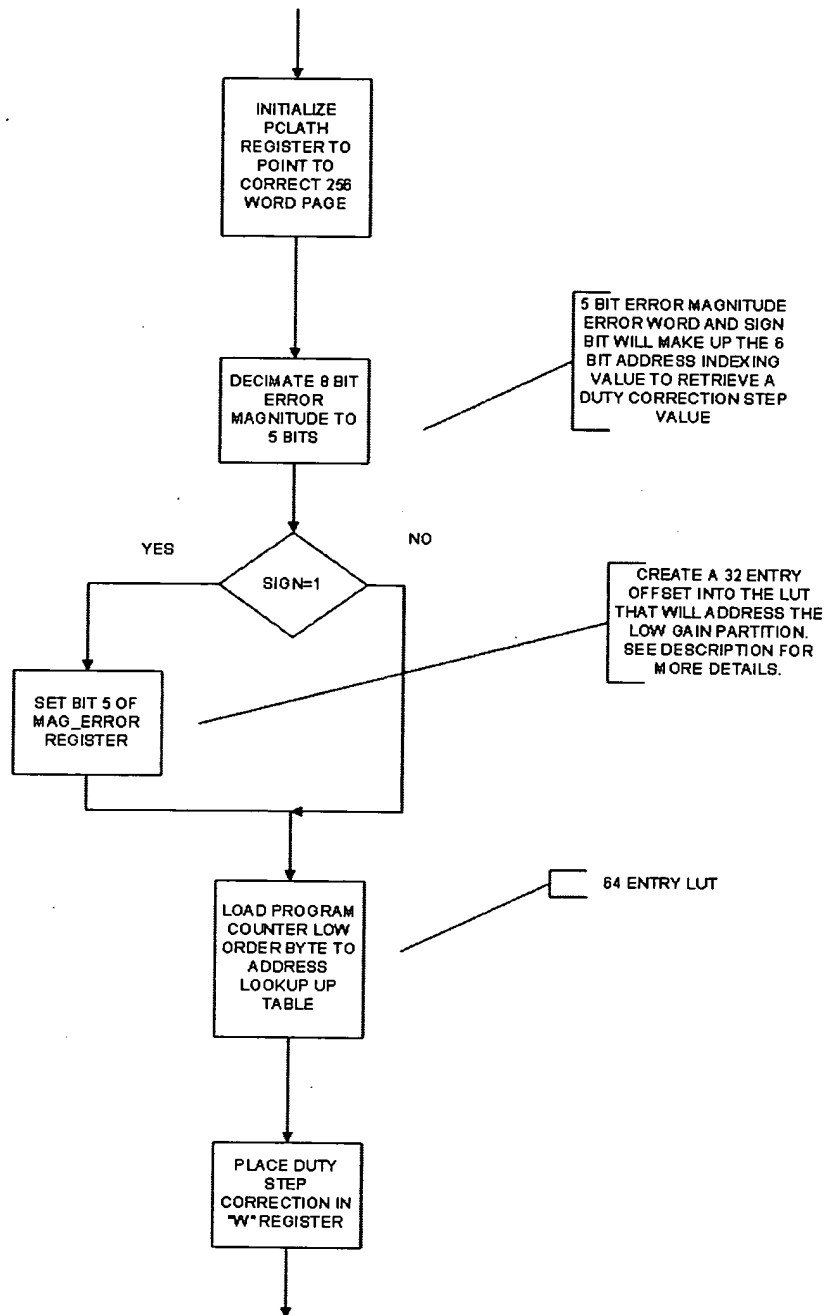


Figure 21

ADC

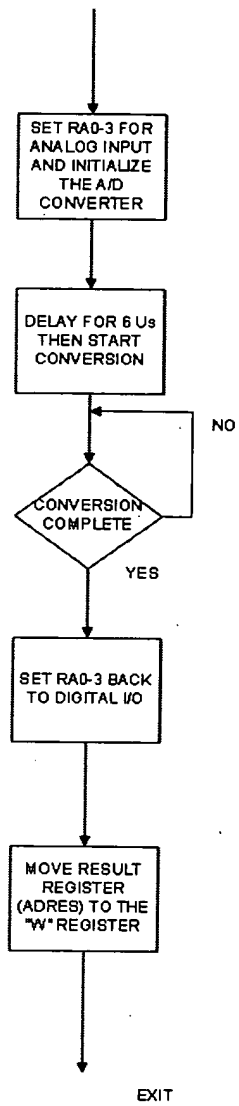


Figure 22

PWM

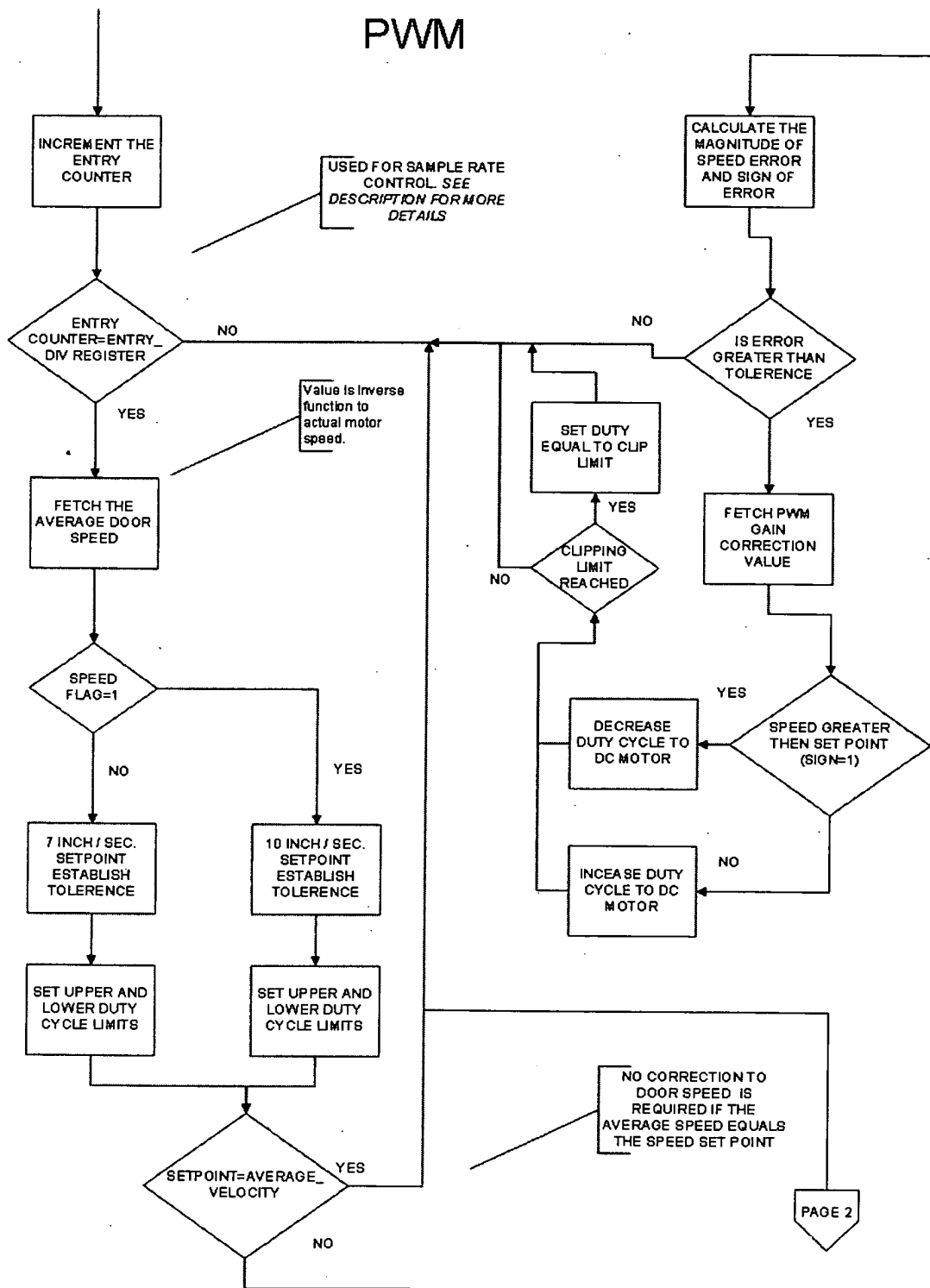


Figure 23

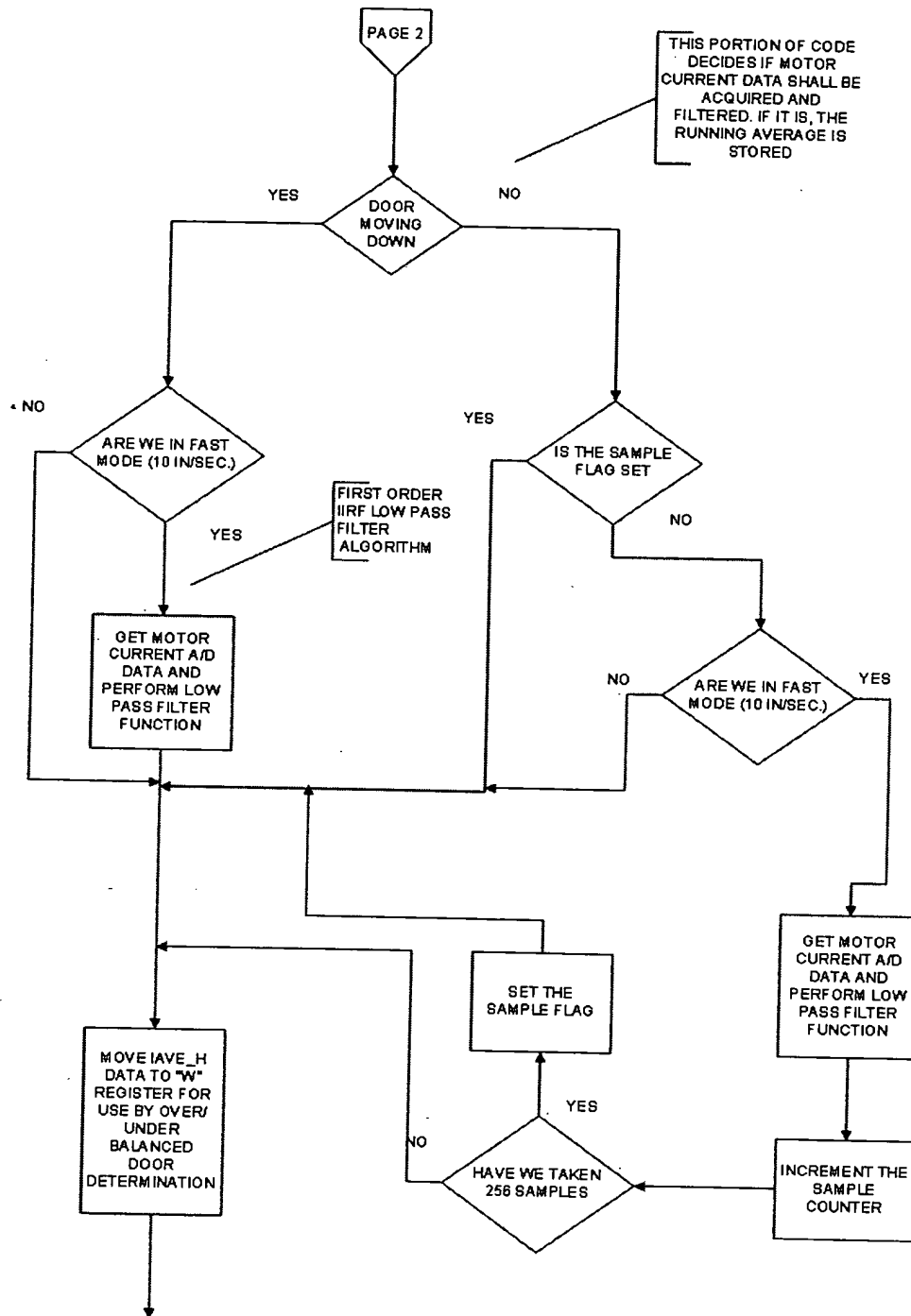


Figure 24

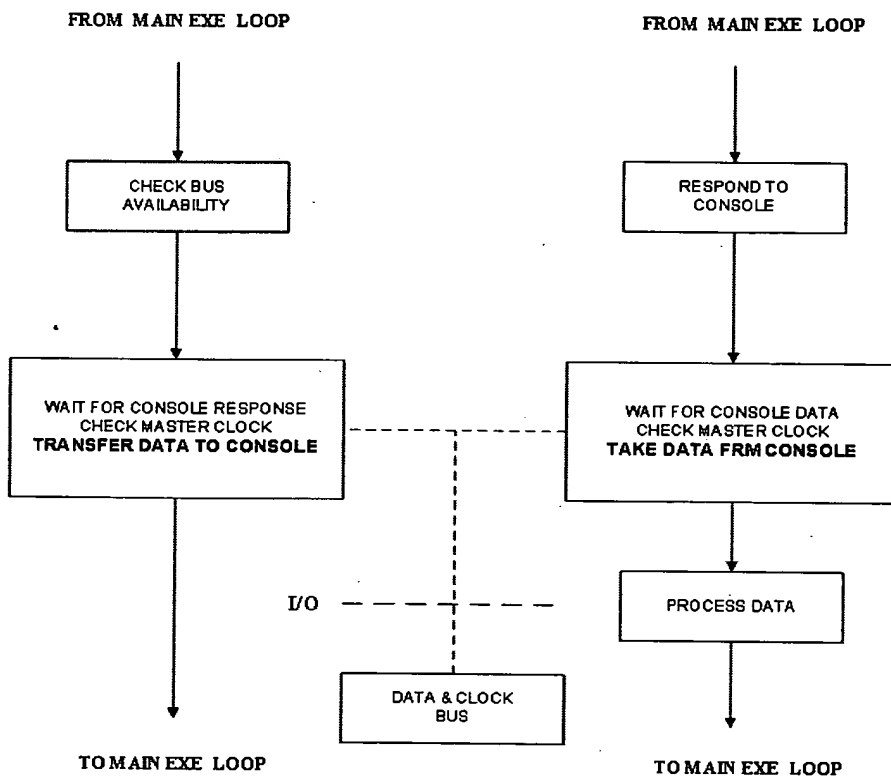


Figure 25

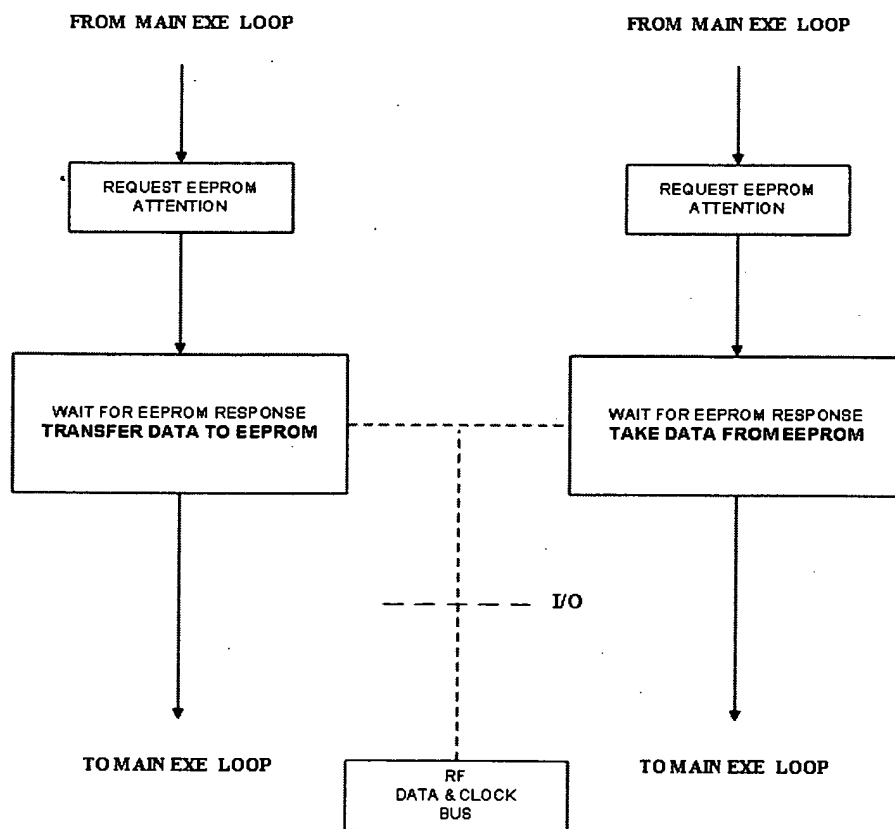


Figure 26

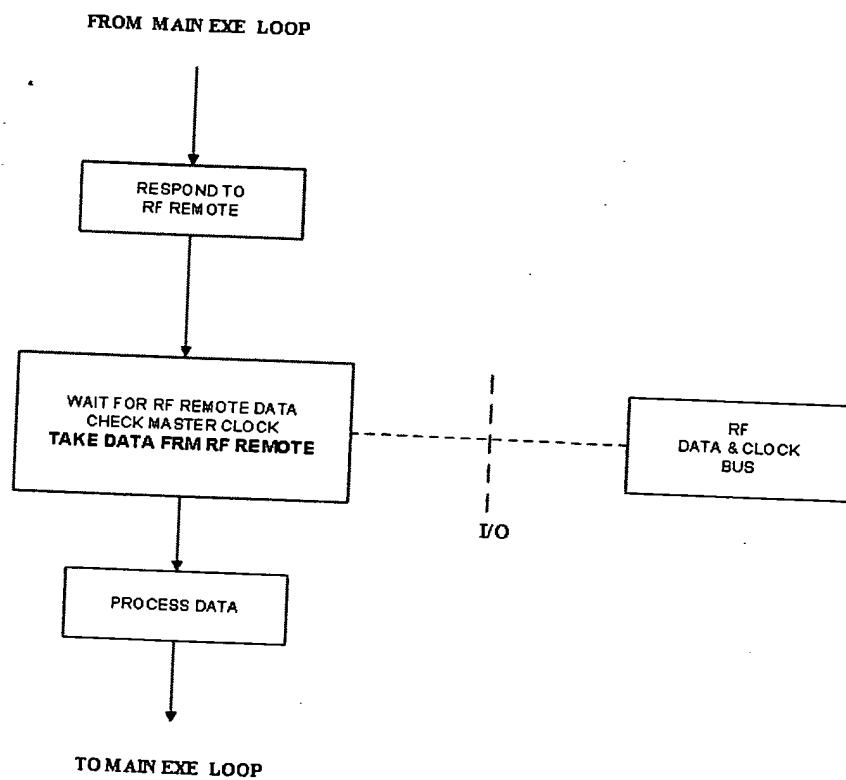


Figure 27

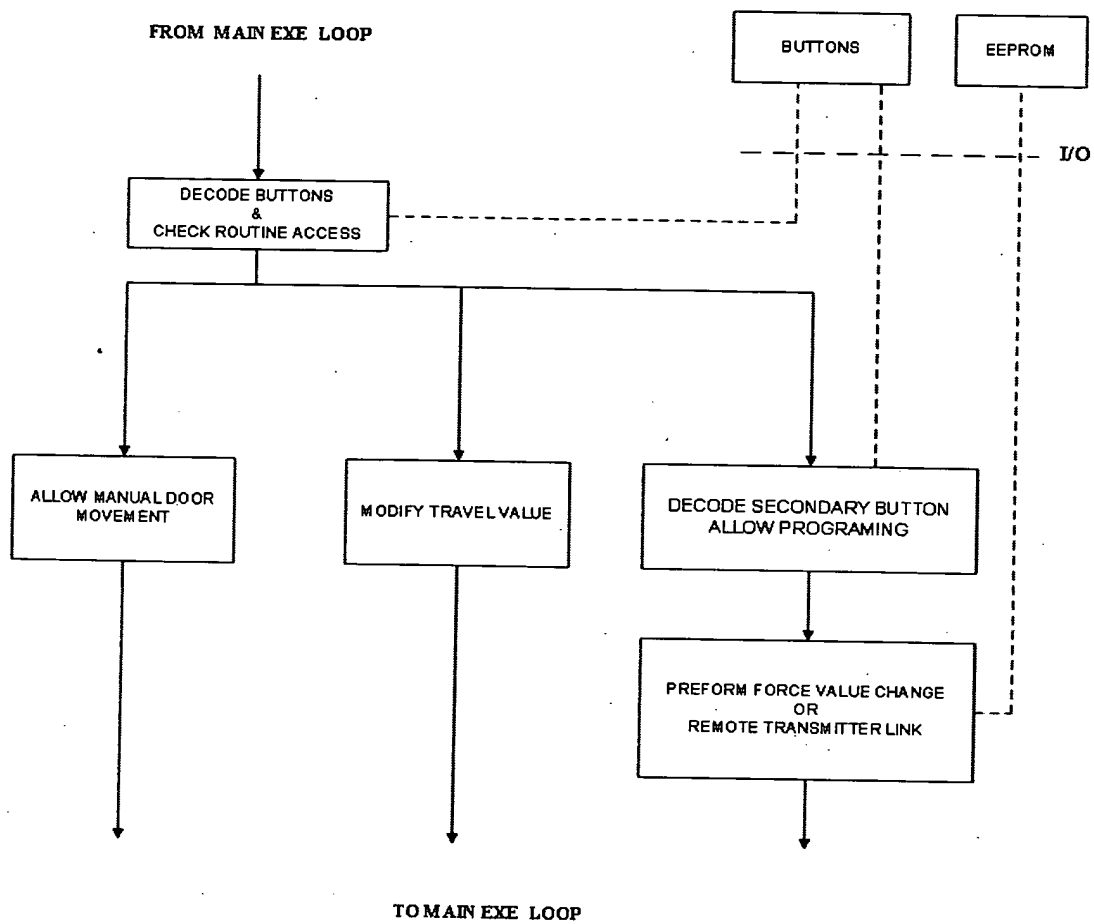


Figure 28

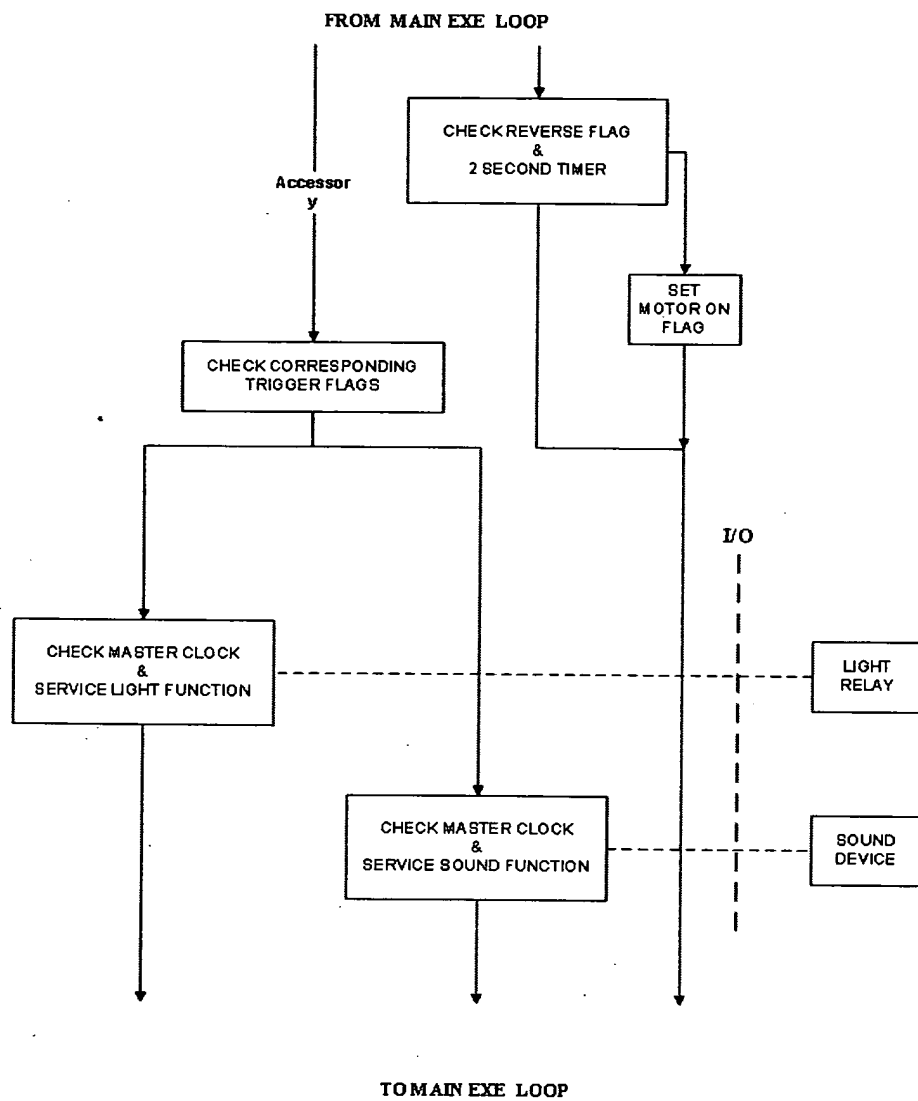


Figure 29

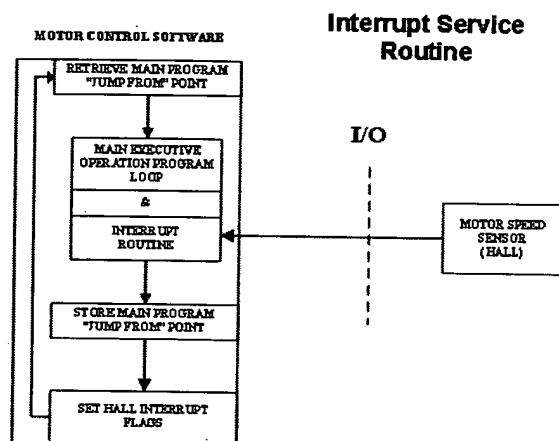


Figure 30

SEND_USART

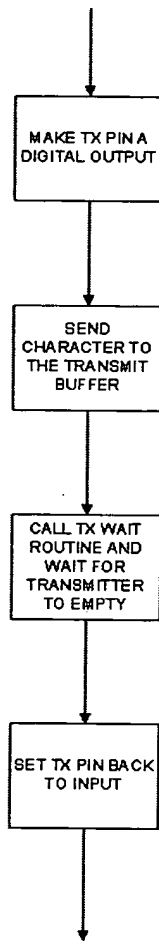


Figure 31

TX_WAIT

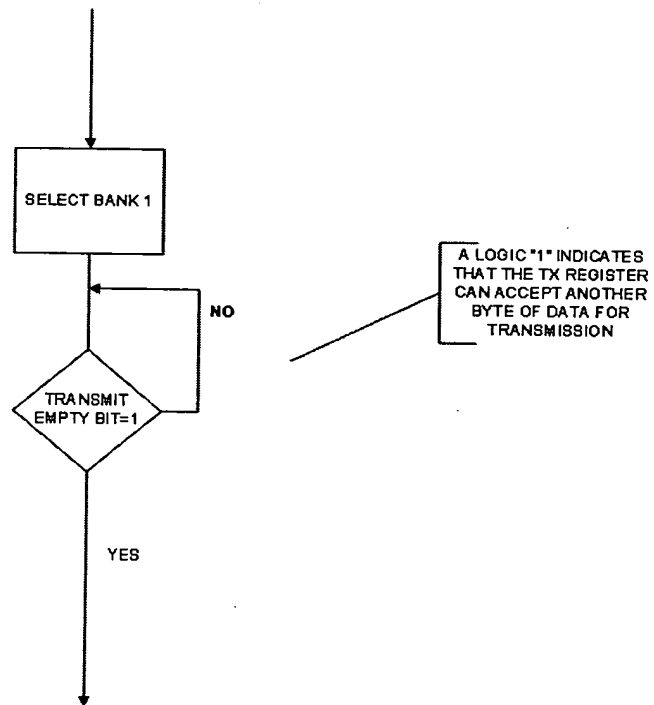


Figure 32

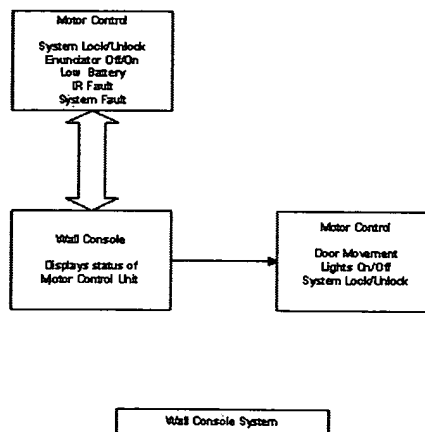


Figure 33

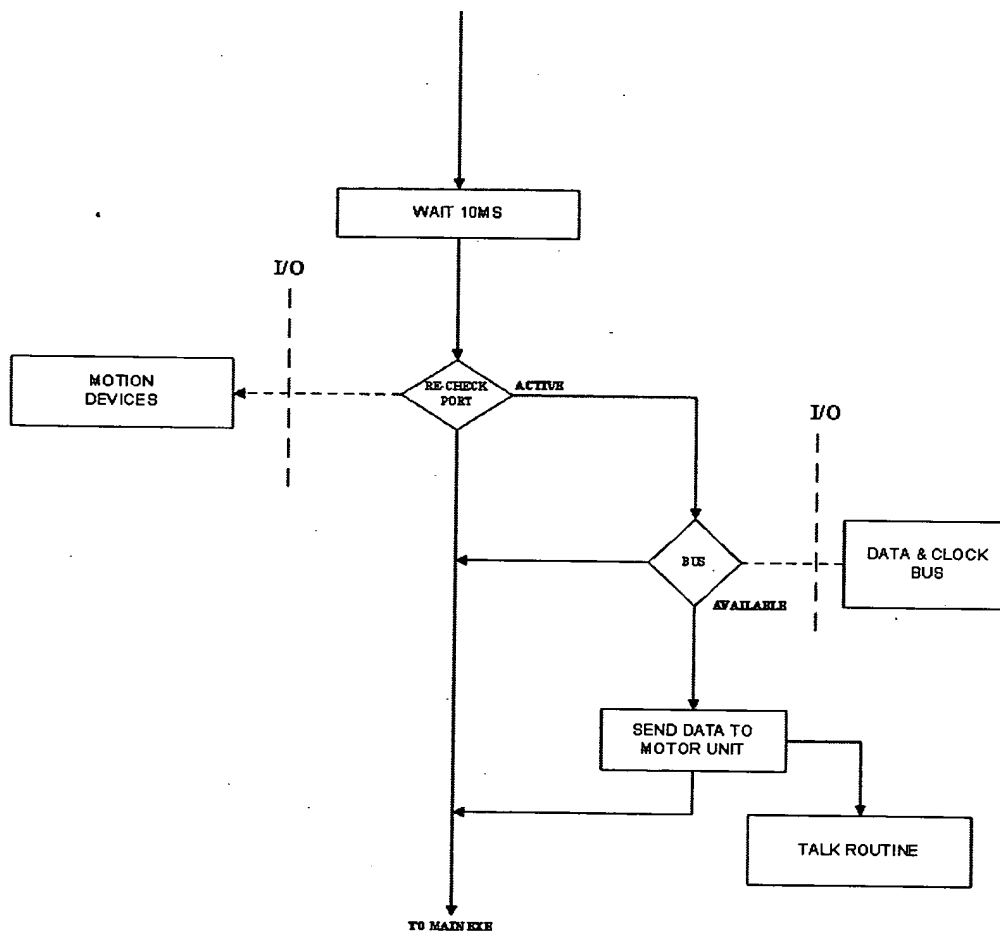


Figure 34

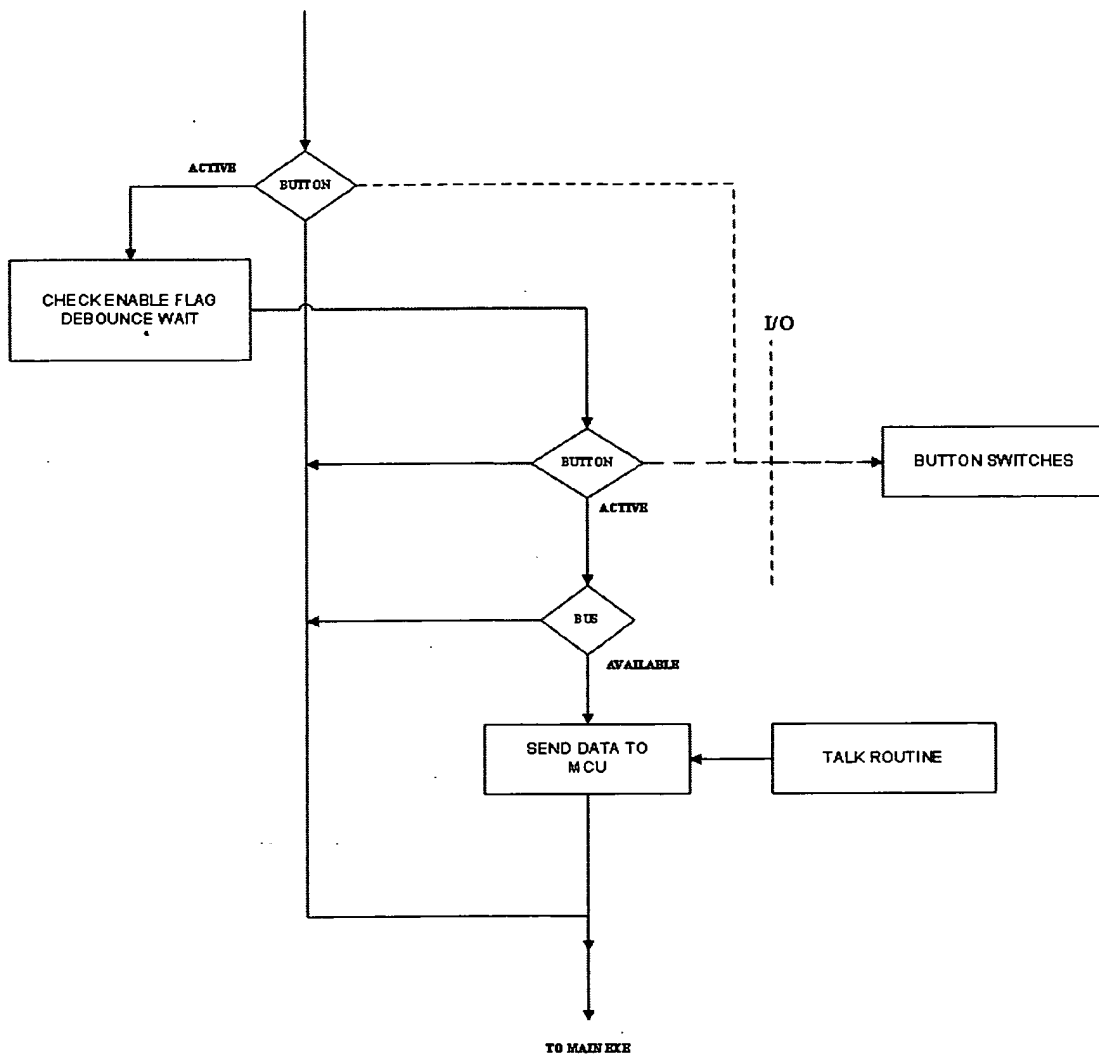


Figure 35

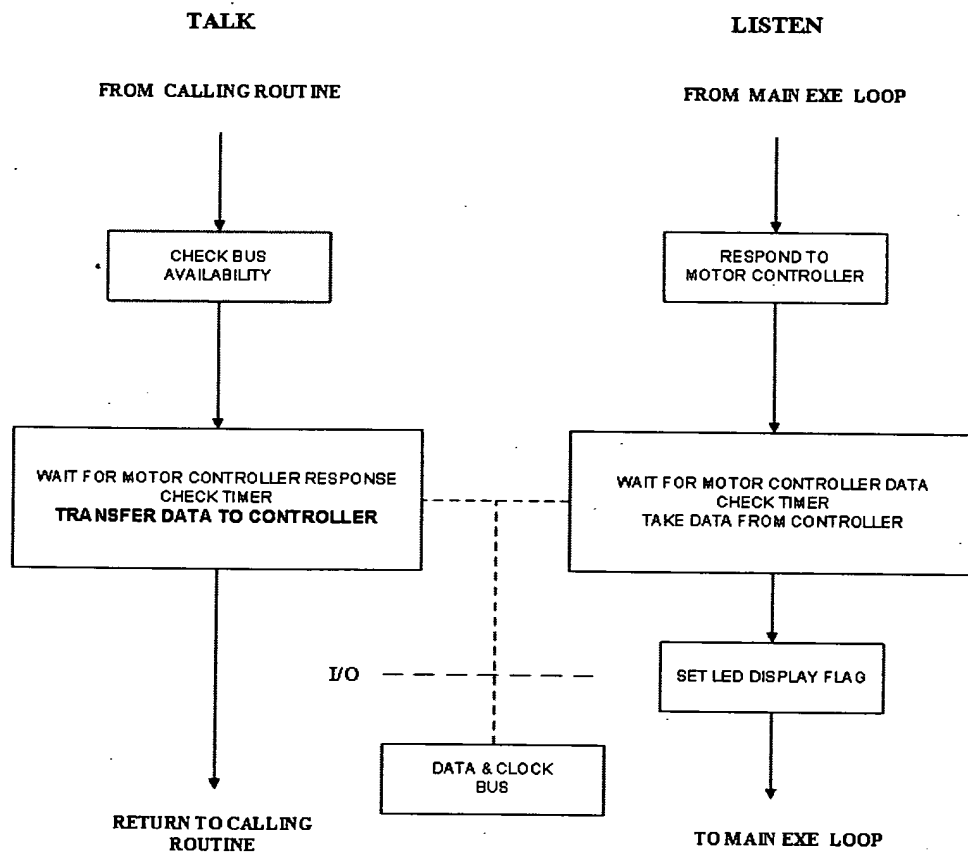


Figure 36

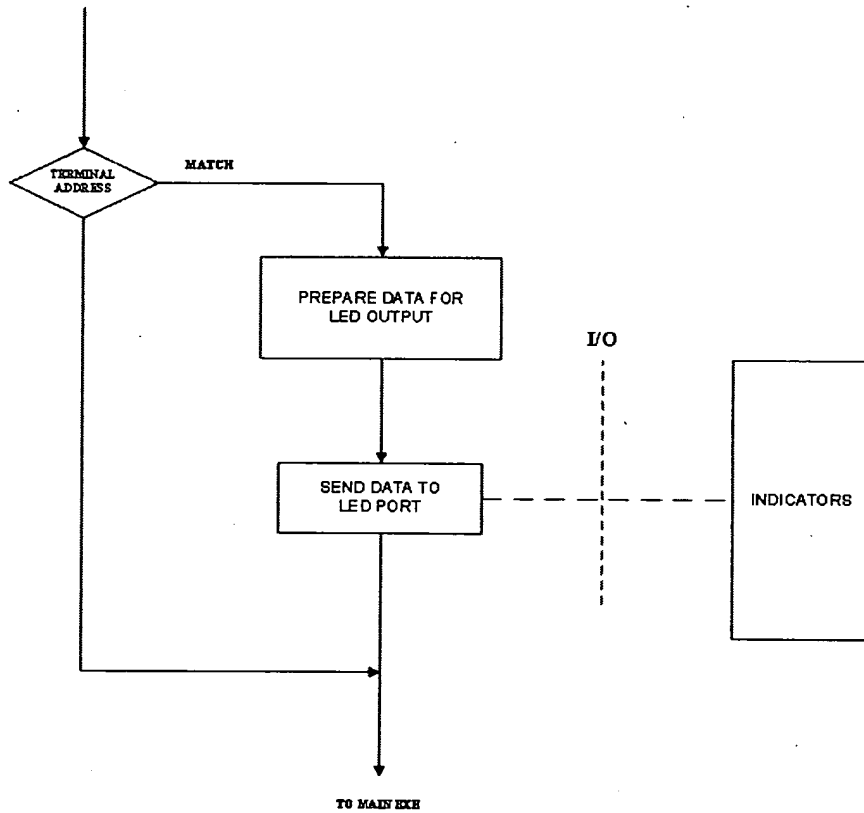


Figure 37

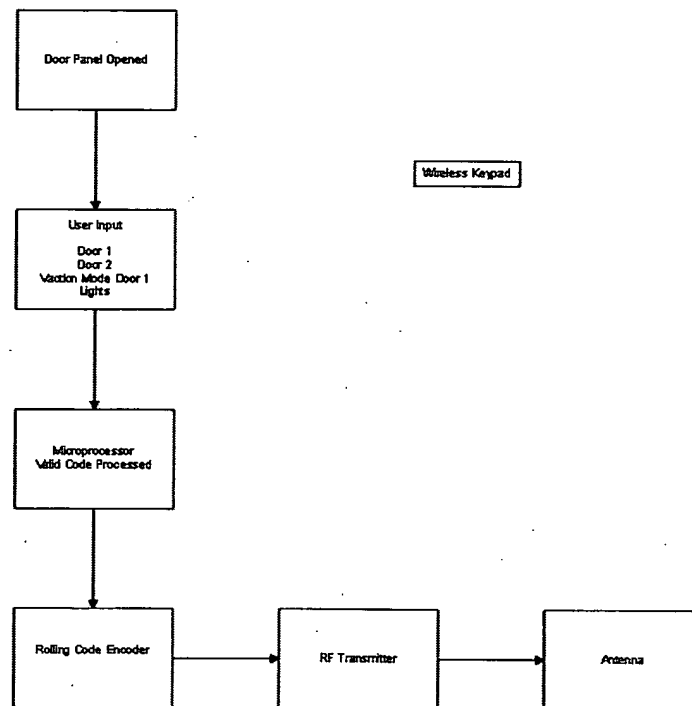


Figure 38

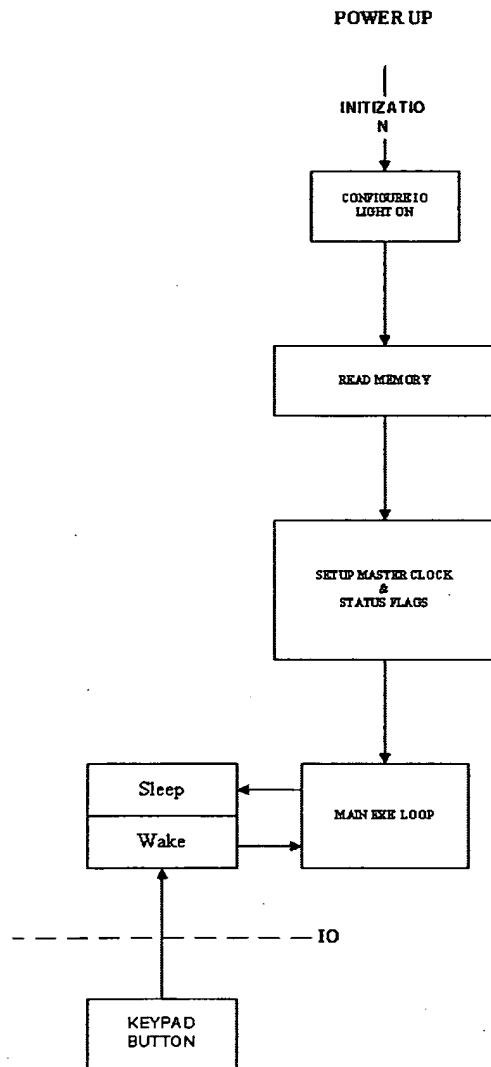


Figure 39

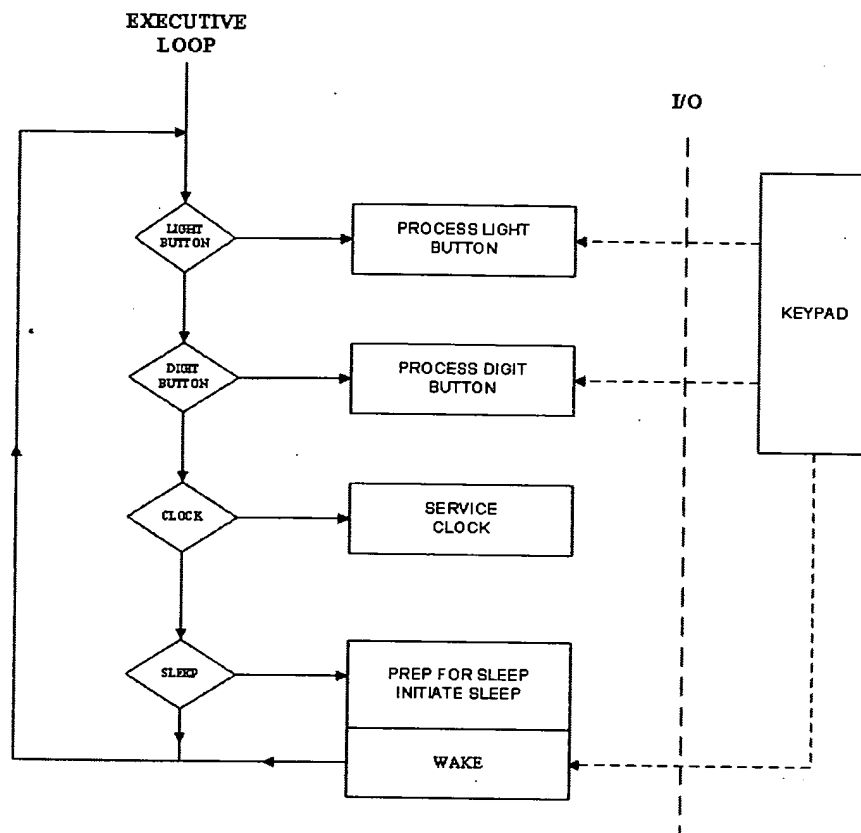


Figure 40

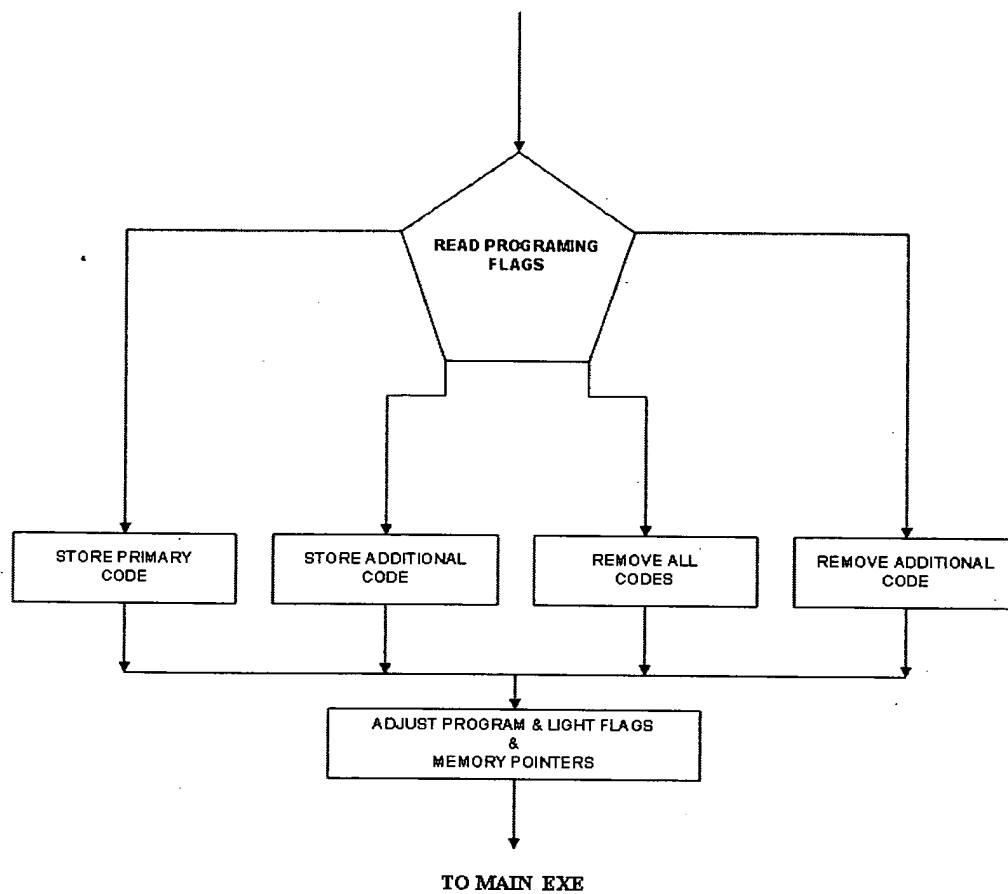


Figure 41

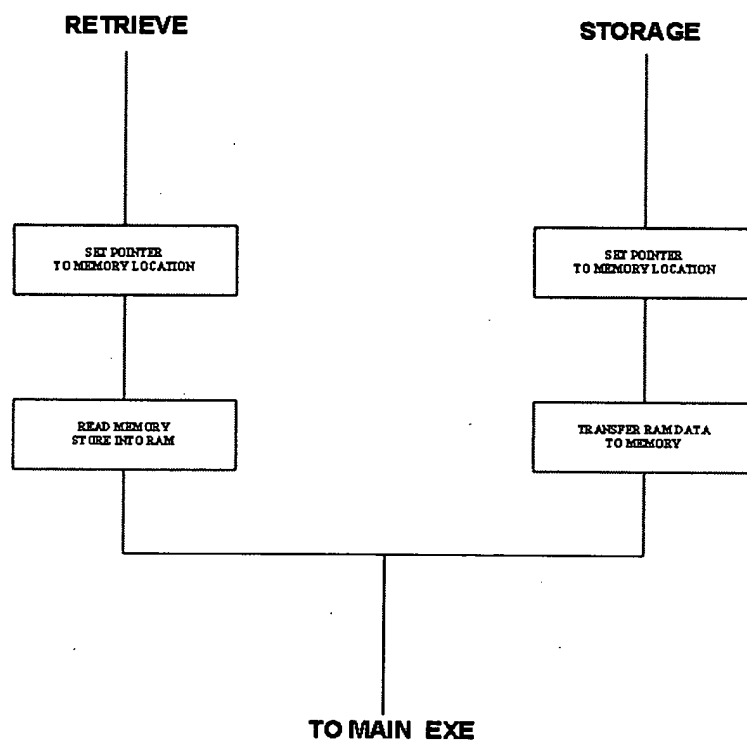


Figure 42

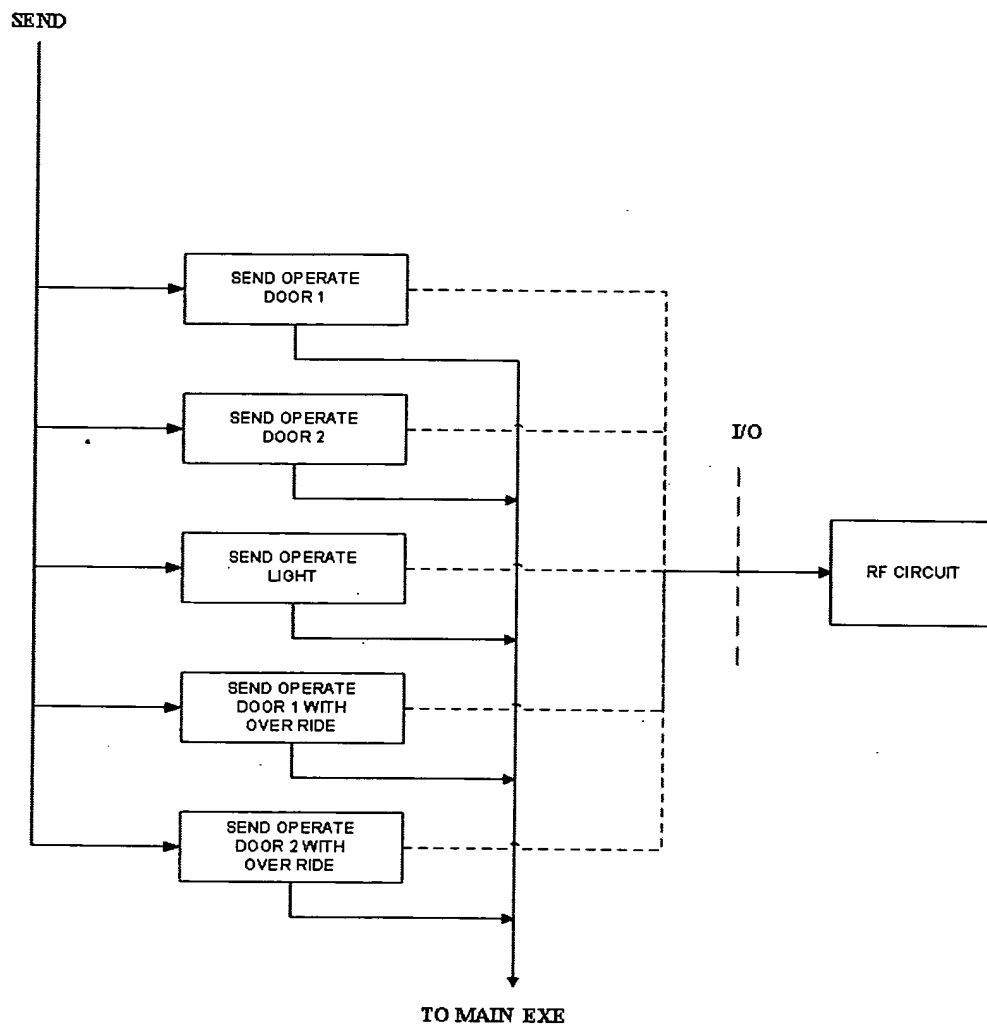


Figure 43

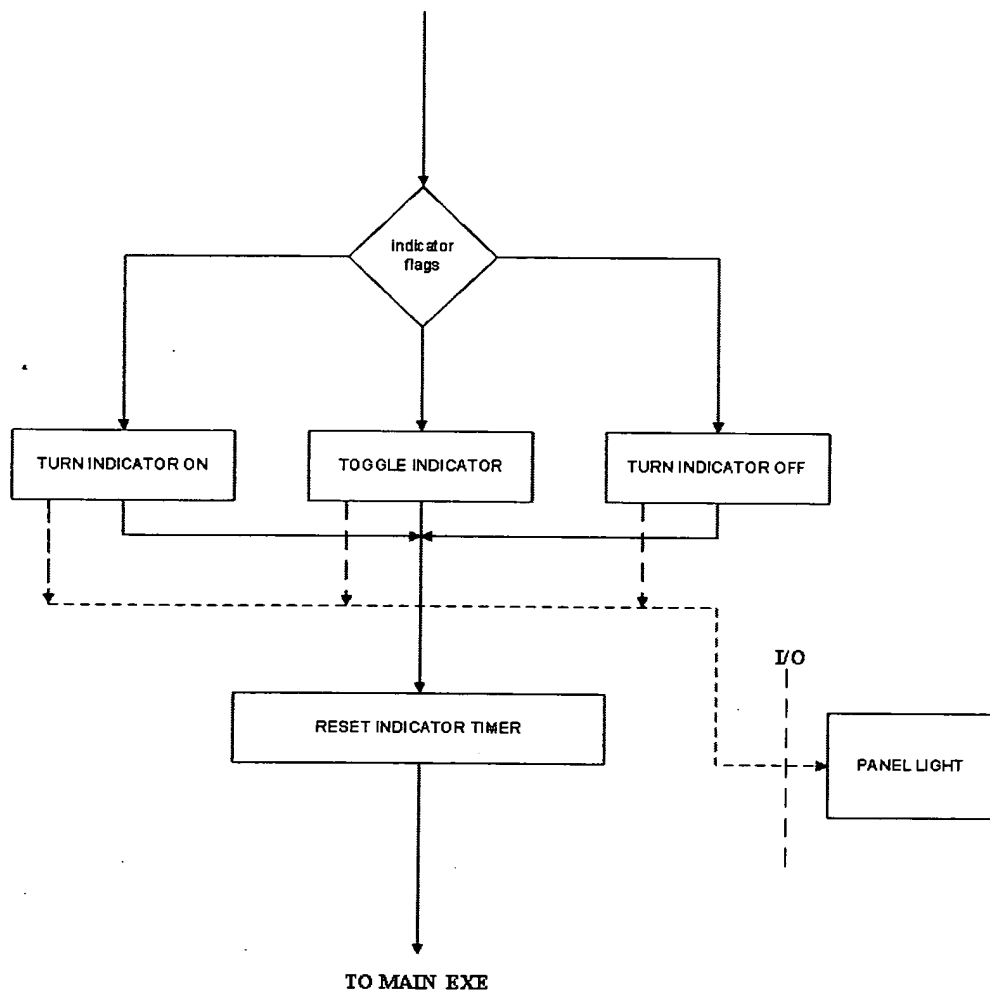


Figure 44

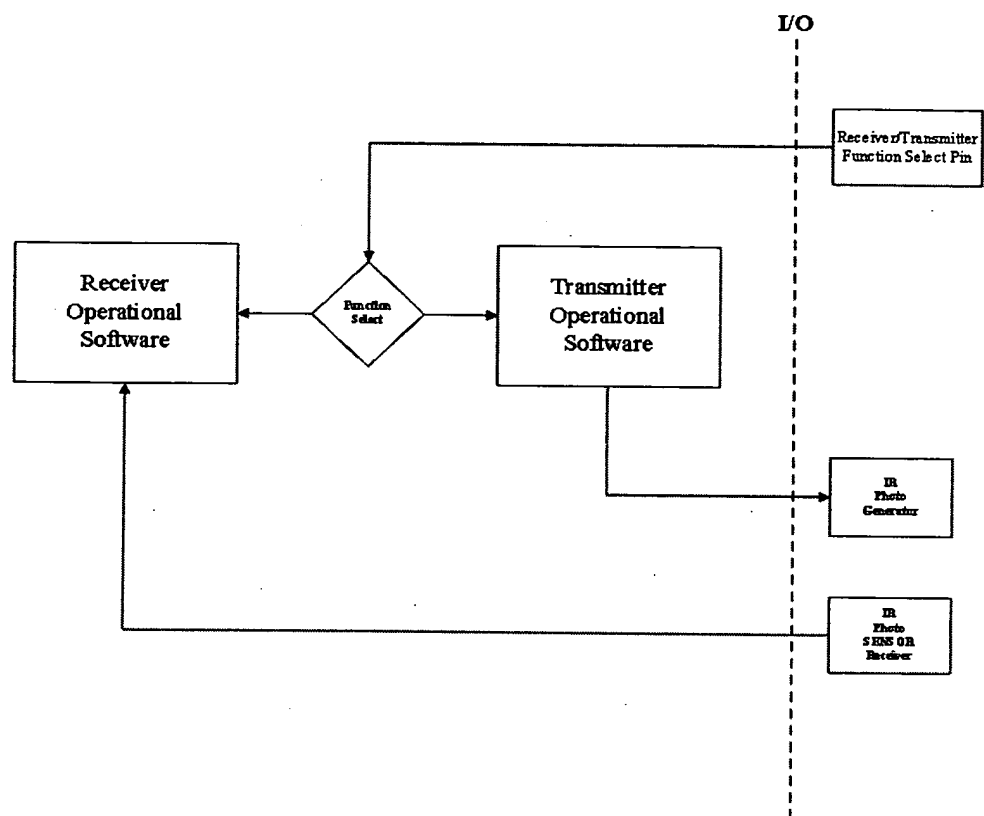


Figure 45